

PIM MODULE 7.5KW 200V

PVD75-6

Futures : Integrated in 3Phase Diode Bridge, Thyristor Switch, Inverter, Brake, and Snubber
For 7.5kw 200V Inverter

Approximate Weight : 400g

MAXMUM RATINGS (Tc=25°C)

Item		Symbol	Rated Value	Unit
3 Phase Rectification Diode	Repetitive Peak Reverse Voltage	V_{RRM}	800	V
	Non-Repetitive Peak Reverse Voltage	V_{RSM}	900	
	Average Rectified Out -Put Current	$I_{O(AV)}$	75	A
	Surge Forward Current	I_{FSM}	800	
	I Squared t	I^2t	3200	A ² s
	Critical Rate of Fall of Forward Current	-di/dt	160(@ :I _{FM} =25A, V _R =500V)	A/μs
Switch Thyristor	Repetitive Peak Off-State Voltage	V_{DRM}	800	V
	Non-Repetitive Peak Off-State Voltage	V_{RSM}	900	
	Average Rectified Out -Put Current	$I_{O(AV)}$	75	A
	Surge Forward Current	I_{TSM}	800	
	I Squared t	I^2t	3200	A ² s
	Critical Rate Of Rise Of Turn-On Current	di/dt	100	A/μs
	Peak Gate Power	P_{GM}	5	W
	Average Gate Power	$P_{GM(AV)}$	1	
	Peak Gate Current	I_{GM}	2	A
	Peak Gate Voltage	V_{GM}	10	V
	Peak Gate Reverse Voltage	V_{RGM}	5	
Inverter IGBT	Collector-Emitter Voltage	V_{CES}	600	V
	Gate-Emitter Voltage	V_{GES}	+/- 20V	
	Collector Current	DC	I_C	A
		1ms	I_{CP}	
	Forward Current	DC	I_F	
		1ms	I_{FM}	
Brake IGBT	Collector Power Dissipation	P_C	300	W
	Collector-Emitter Voltage	V_{CES}	600	V
	Gate Emitter Voltage	V_{GES}	+/- 20V	
	Collector Current	DC	I_C	A
		1ms	I_{CP}	
	Collector Power Dissipation	P_C	178	W
Snubber Diode	Repetitive Peak Reverse Voltage	V_{RRM}	600	V
	Forward Current, DC	I_F	15	A
	Surge Forward Current	I_{FSM}	150	
Operating Junction Temperature Range		T_{JW}	-40 to +150°C (notes: +125 °C > Can not be biased.)	°C
Storage Temperature Range		T_{stg}	-40 to +125°C	
Isolation Voltage(Terminal to Base)		Viso	2500(@AC, 1minute), 3000(@AC, 1second)	V
Isolation Resistance(Terminal to Base, @DC=500V)		Riso	500	M.ohm
Mounting Torque(Module Base to Heatsink)		Ftor	(M4), 1.4	N·m

ELECTRICAL CHARACTERISTICS (Tc=25°C Unless otherwise noted)

Characteristic		Symbol	Test Condition		Min.	Typ.	Max.	Unit
3 Phase Rectification Diode	Peak Reverse Current *1	I _R	T _J =150°C, V _{RM} =V _{RRM}		-	-	10	mA
	Peak Reverse Voltage *1	V _F	I _F =80A		-	-	1.40	μA
Switch Thyristor	Peak OFF-State Current	I _{DM}	T _J =125°C, V _{DM} =V _{DRM}		-	-	50	mA
	Peak Reverse Current	I _{RM}	T _J =125°C, V _{RM} =V _{RRM}		-	-	50	
	Peak On-State Voltage	V _{TM}	I _T =80A		-	-	1.40	V
	Gate Current to Trigger	I _{GT}	V _D =6V I _T =1A	T _J =-40°C	-	-	200	mA
				T _J =25°C	-	-	100	
				T _J =125°C	-	-	50	
	Gate Voltage to Trigger	V _{GT}	V _D =6V I _T =1A	T _J =-40°C	-	-	40	V
				T _J =25°C	-	-	25	
				T _J =125°C	-	-	20	
	Gate Voltage to Non-Trigger	V _{GD}	T _J =125°C, V _D =2/3V _{DRM}			0.25	-	-
Critical Rate Of Rise Of Off-State Voltage	dv/dt	T _J =125°C, V _D =2/3V _{DRM}			500	-	-	V/μs

TENTATIVE



Switch Thyristor	Turn-Off Time		tq	T _J =125°C, V _D =2/3V _{DRM} V _{RM} =100V, dv/dt=20V/μs -di/dt=20A/μs	-	100	-	μs
	Turn-On Time		tgt	T _J =25°C, V _D =2/3V _{DRM}	-	6	-	
	Delay Time		td	I _C =200mA	-	2	-	
	Rise Time		tr	-di/dt=0.2A/μs	-	4	-	
	Latching Current		I _L		-	100	-	mA
	Holding Current		I _H		-	80	-	
Inverter IGBT	Collector-Emitter Out-Off Current		I _{CES}	V _{CE} =600V, V _{GE} =0V	-	-	1.0	mA
	Gate-Emitter Leakage Current		I _{GES}	V _{GE} =+/- 20V, V _{CE} =0V	-	-	0.5	μA
	Collector-Emitter Saturation Voltage		V _{CE(sat)}	I _C =75A, V _{GE} =15V	-	2.1	2.6	V
	Gate-Emitter Threshold Voltage		V _{GE(th)}	V _{CE} =5V, I _C =75mA	4.0	-	8.0	V
	Input Capacitance		C _{ies}	V _{CE} =10V, V _{GE} =0V, f=1MHz	-	7500	-	pF
	Switching Time	Rise Time	t _r	V _{CC} = 300V	-	0.15	0.30	μs
		Turn-On Time	t _{on}	R _L = 2 ohm	-	0.25	0.40	
		Fall Time	t _f	R _G = 10 ohm	-	0.20	0.35	
		Turn-Off Time	t _{off}	V _{GE} = +/- 15V	-	0.45	0.7	
	Peak Forward Voltage		V _F	I _F =75A	-	1.9	2.4	V
	Reverse Recovery Time		t _{rr}	I _F =75A, V _{CE} =-10V, di/dt=75A/μs	-	0.15	0.25	μs
Brake IGBT	Collector-Emitter Cut-Off Current		I _{CES}	V _{CE} =600V, V _{GE} =0V	-	-	1.0	mA
	Gate-Emitter Leakage Current		I _{GES}	V _{GE} =+/- 20V, V _{CE} =0V	-	-	0.5	μA
	Collector-Emitter Saturation Voltage		V _{CE(sat)}	I _C =30A, V _{GE} =15V	-	2.0	2.5	V
	Gate-Emitter Threshold Voltage		V _{GE(th)}	V _{CE} =5V, I _C =30mA	4.0	-	8.0	V
	Input Capacitance		C _{ies}	V _{CE} =10V, V _{GE} =0V, f=1MHz	-	4000	-	pF
	Switching Time	Rise Time	t _r	V _{CC} = 300V	-	0.15	0.3	μs
		Turn-on Time	t _{on}	R _L = 10 ohm	-	0.25	0.4	
		Fall Time	t _f	R _G = 15 ohm	-	0.20	0.35	
		Turn-off Time	t _{off}	V _{GE} = +/- 15V	-	0.45	0.7	
Snubber Diode	Peak Forward Voltage		V _F	I _F =15A	-	-	2.5	V
	Reverse Recovery Time		t _{rr}	I _F =15A, di/dt=50A/μs	-	-	0.3	μs

*1: per 1arm

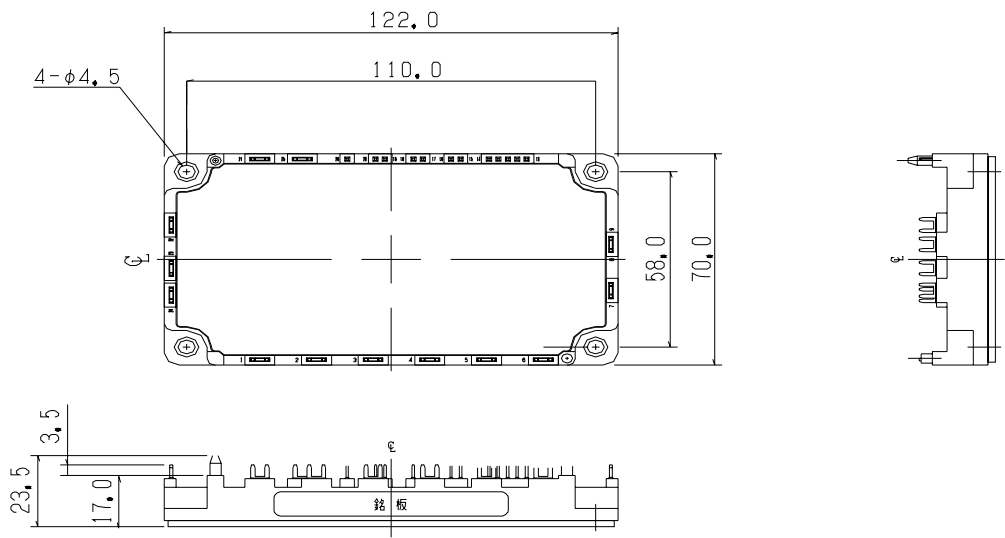
ELECTRICAL CHARACTERISTICS (T_c=25°C Unless otherwise noted)

Thermister	Resistance	25°C	-	5.00	-	k. ohm
		75°C	-	0.97	-	
		125°C	-	0.27	-	
	B-Value	25°C/50°C	-	3375	-	K
		25°C/85°C	-	3420	-	
	Thermal Time Constant		-	10	-	s

THERMAL CHARACTERISTICS

Characteristic			Test Condition		Min.	Typ.	Max.	Unit
Thermal Impedance	R _{th(j-c)} Junction to Case	3 Phase Rectification Diode	Per :1 arm.		-	-	0.75	°C/W
		Switch Thyristor			-	-	0.55	
		Inverter IGBT			-	-	0.42	
		Inverter Free Wheeling Diode			-	-	0.90	
		Brake IGBT			-	-	0.70	

PVD75-6 OUTLINE DRAWING
(Dimensions in mm)



CIRCUIT

